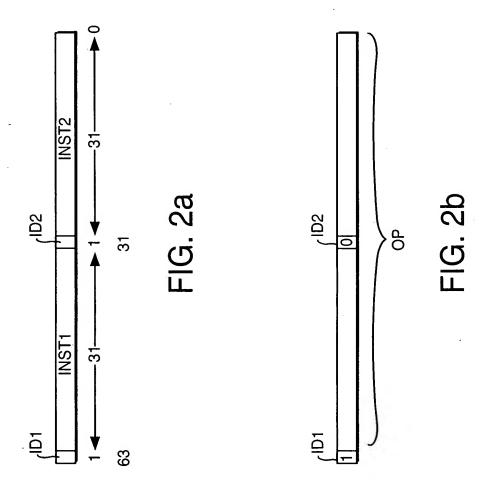


FIG. 1



31   30	31   30   29   28   27   26   25   24   23   22   21   20	1 23 22	2   21   2		17   16	5   15   14   13   12	11	10   09   08   07   06	19 18 17 16 15 14 13 12 11 10 09 08 07 06 05 04 03 02 01 00	· [ <del>                                    </del>
	OPCODE		<u>s</u>	TST		SRC2	· ——	SRC1	DST	(1).
υ — Σ	OPCODE		TST		 	IMMED	 	SRC1	DST	(2)
_ G 	OPCODE		<u>s</u>	TST		SRC2		SRC1	SIGN   M REG	(3)
<u>E</u>	OPCODE		<u>s</u>	TST	SHIFT	FT   M REG		SRC1	DST	3/
<u>W</u>	OPCODE		<u>s</u>	TST		SRC2	_	SRC1	DST	<b>4</b> (2) —
Σ	OPCODE	<u>-</u>		IMMEDIATE		OFFSET		BASE REG	DST	(9)
Σ	OPCODE		INDX	OP T	<u></u>	INDX REG	_	BASE REG	DST	(7)

FIG. 3

ВІТ	31(M)	30 <u>(</u> G)	29(G)
REGISTER/ IMMEDIATE	1	0	×
REGISTER/ REGISTER	ļ	<b>,</b>	0
MAC OPERATIONS	1	1	-

## FIG. 4

L1	L2	- L3
<u>-</u> 0	-	- 1
-5	DEST1	DEST1
-116	SRC1	SRC1
3-17-12 E	SRC2	10W16
281 IMMEDIA	RESERVED	T   IMMEDIATE HI16    1111   IMMEDIATE   OW16   SRC1
2-31-26 1111	1111	1111
-3732  IMMED	DEST2	1161
1-43-38 OITION	SRC3	)IATE H
)-49-44 CH CONE	SRC4	IMME
-53   B	SITST	ISITSI
-62	0  OPERATION  S TST  SRC4  SRC3  DEST2  111  RESERVED  SRC2  SRC1   DEST1	OPERATION
-63 10	0	01

FIG. E